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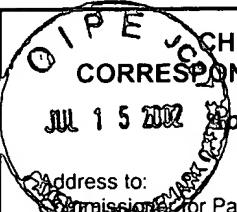
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PTO/SB/122 (11-96)

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	Filing Date	April 5, 2001
	First Named Inventor	Tatsuya ARAO et al.
	Group Art Unit	2811
	Examiner Name	T. Tran
	Attorney Docket Number	0756-2293

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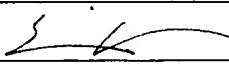
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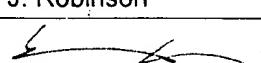
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CONFIRMATION NO. 5035

Bib Data Sheet

SERIAL NUMBER 09/826,416	FILING DATE 04/05/2001 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 0756-2293
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APPLICANTS

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**** CONTINUING DATA *******

None T

**** FOREIGN APPLICATIONS *******

JAPAN 2000-140592 05/12/2000

Yes T

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** 05/18/2001

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 29	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
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TITLE

Semiconductor device and manufacturing method therefor

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